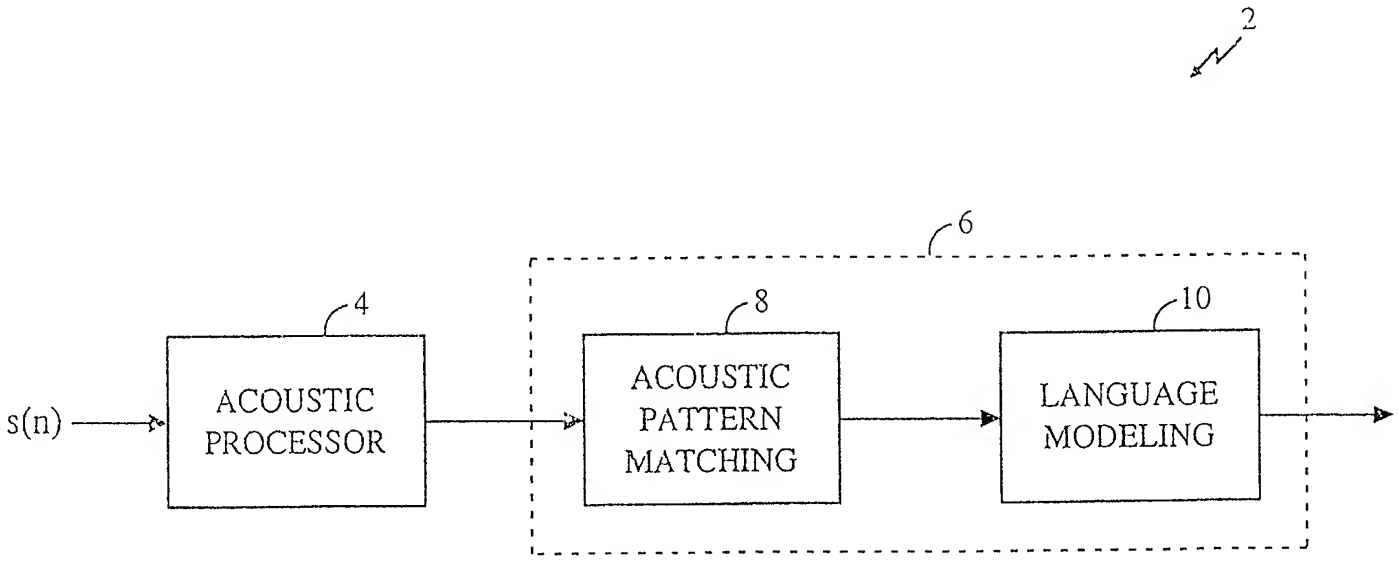
2
6
4
8
10
s(n)
ACOUSTIC PROCESSOR
ACOUSTIC PATTERN MATCHING
LANGUAGE MODELING

FIG. 1

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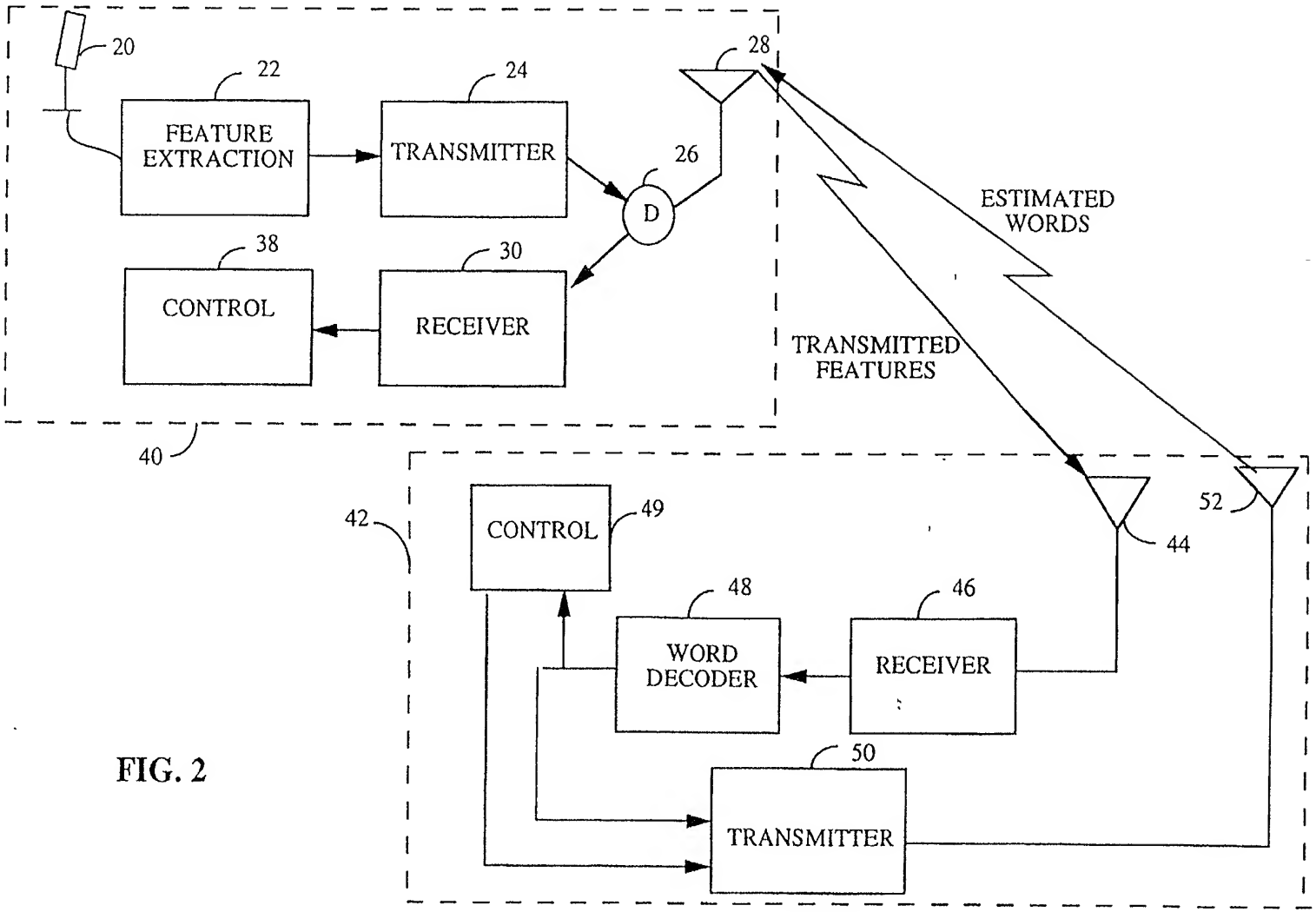

FIG. 2

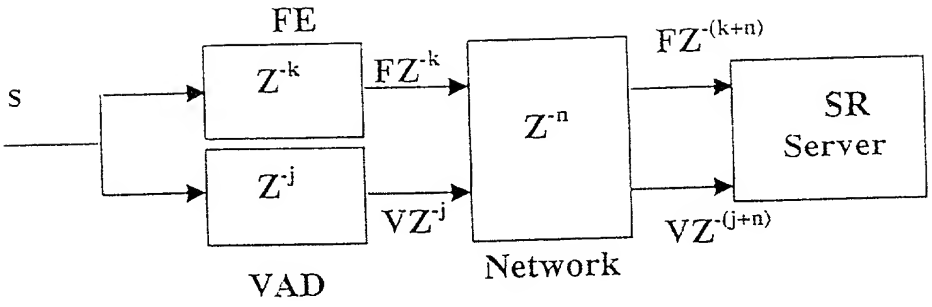

FIG. 3

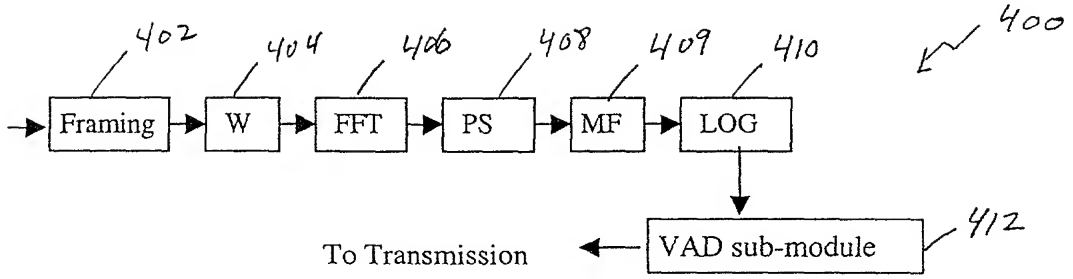

Figure 4. Block diagram of the VAD

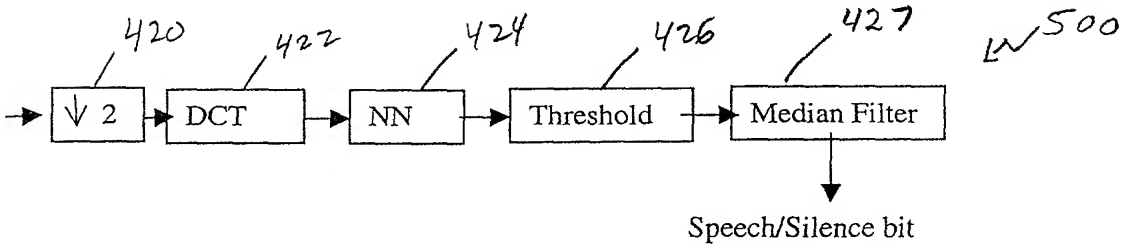

Figure 5. Block diagram of Voice Activity Detector sub-module

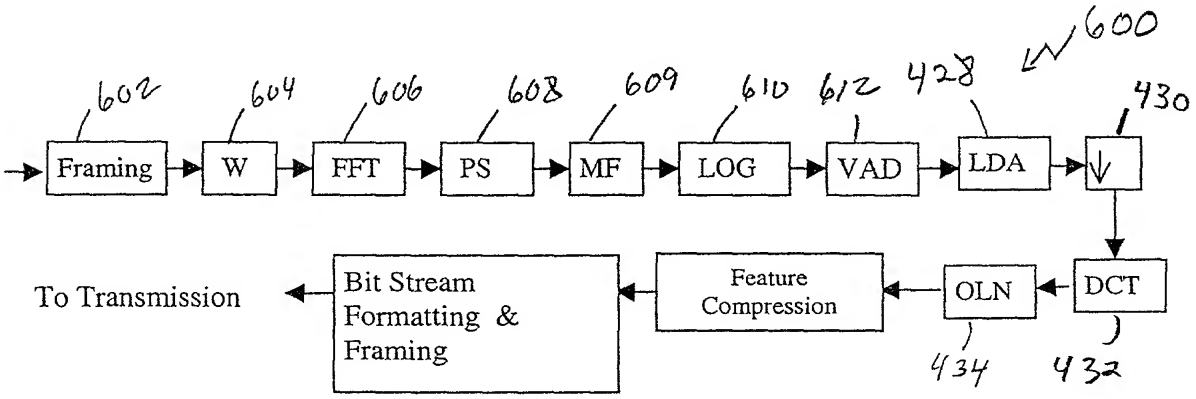

Figure 6. Block diagram of the FE module
(Terminal Feature Extraction and Terminal Compression)

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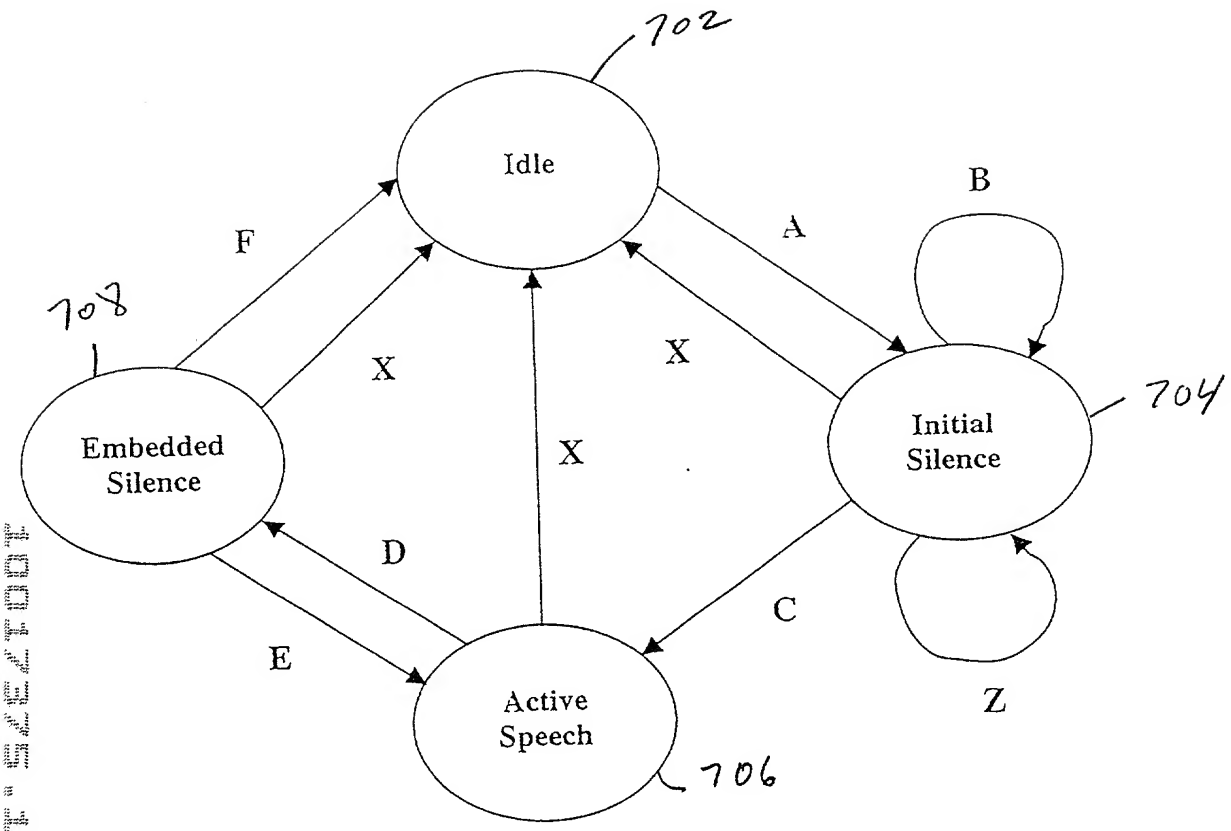

FIG. 7

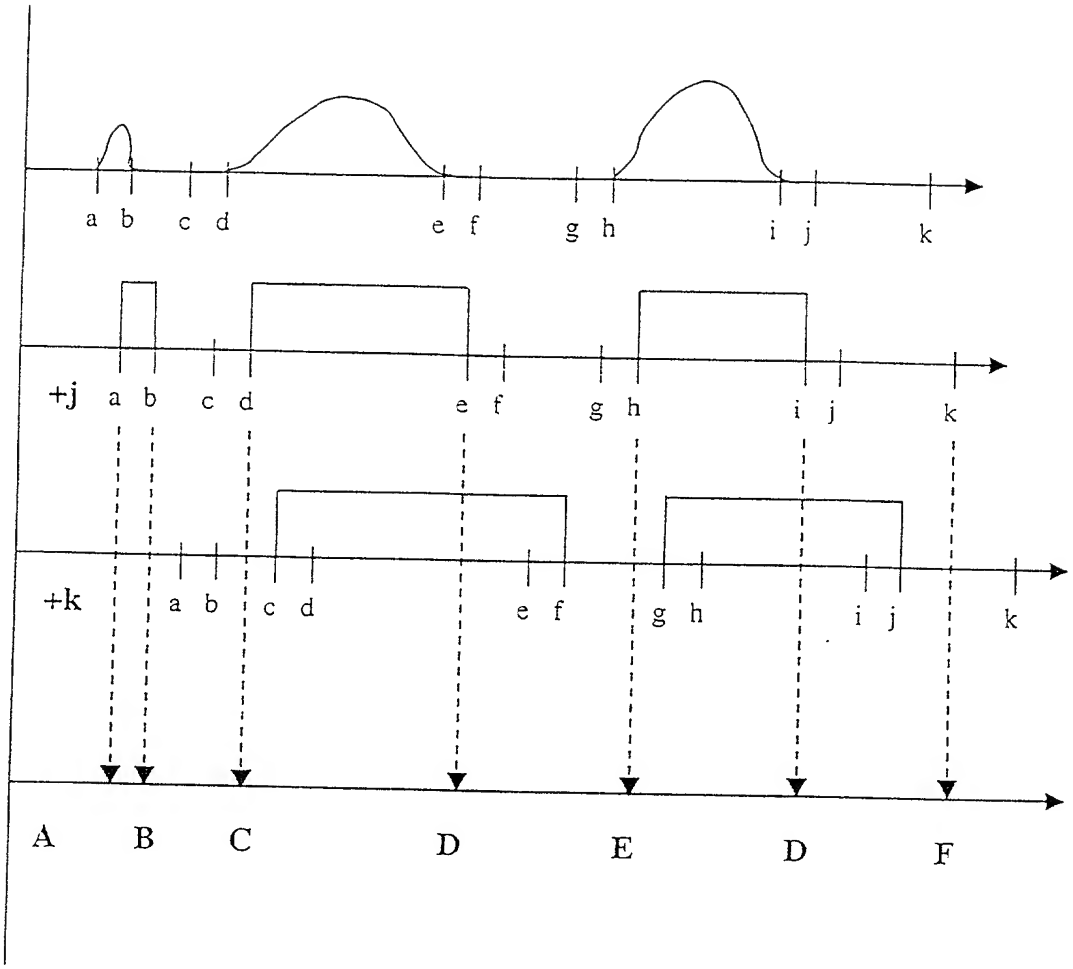
Energy
VAD
FE Payload
Events
a b c d e f g h i j k
+j a b c d e f g h i j k
+k a b c d e f g h i j k
A B C D E D F

FIG. 8